

Features

- 3 kA, 8/20 µs surge capability
- Low clamping voltage under surge
- Bidirectional TVS
- Excellent performance over temperature
- UL Recognized (pending)

Applications

■ High power DC bus protection

PTVS3-xxxC-TH Series High Current TVS Diodes

General Information

The Model PTVS3-xxxC-TH Series high current bidirectional TVS diodes are designed for use in high power DC bus clamping applications. These devices offer bidirectional port protection and are available with standoff voltage ratings of 58 V and 76 V.

The devices are RoHS* compliant and UL Recognized (pending). They also meet IEC 61000-4-5 $8/20~\mu s$ current surge requirements.



Absolute Maximum Ratings (@ T_A = 25 °C Unless Otherwise Noted)

Rating		Symbol	Value	Unit
Repetitive Standoff Voltage	PTVS3-058C-TH PTVS3-076C-TH	V _{WM}	58 76	V
Peak Current Rating per 8/20 µs IEC 61000-4-5		I _{PPM}	3	kA
Operating Junction Temperature Range		TJ	-40 to +125	°C
Storage Temperature Range		T _S	-55 to +150	°C
Lead Temperature, Soldering (10 s)			260	°C

Electrical Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

Paran	neter	Test	Conditions	Min.	Тур.	Max.	Unit
I _D	Standby Current	$V_D = V_{WM}$				10	μΑ
V _(BR)	Breakdown Voltage	I _{BR} = 10 mA	PTVS3-058C-TH PTVS3-076C-TH	64 85	67 90	70 95	V
V _C	Clamping Voltage ⁽¹⁾ per IEC61000-4-5 (8/20 µs current waveform)	I _{PP} = 3 kA	PTVS3-058C-TH PTVS3-076C-TH			110 140	V
V _(BR)	Temperature Coefficient				0.1		%/°C
С	Capacitance	F = 10 kHz, V _d = 1 Vrms	PTVS3-058C-TH PTVS3-076C-TH		2.3 1.7		nF

 $^{^{(1)}}$ V_C measured at the time which is coincident with the peak surge current.

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The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time. Users should verify actual device performance in their specific applications.

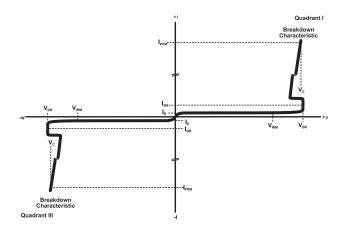
^{*}RoHS Directive 2002/95/EC Jan. 27, 2003 including annex and RoHS Recast 2011/65/EU June 8, 2011. Specifications are subject to change without notice.

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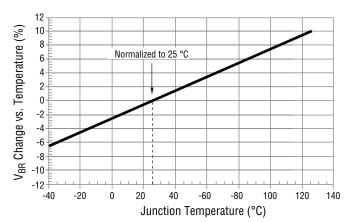
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Performance Graphs

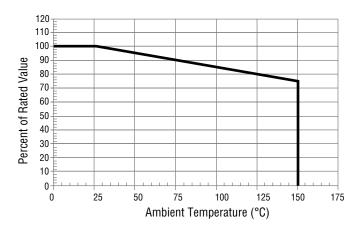
V-I Characteristic



Percentage V_{BR} Change vs. Junction Temperature



Typical Surge Current Derating



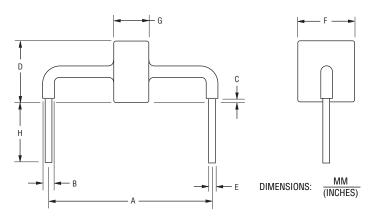
This graph shows the typical device surge current derating versus ambient temperature when subjected to the 8/20 μ s current waveform per the IEC 61000-4-5 specification. This device is not intended for continuous operation at temperatures above 125 °C.

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Product Dimensions

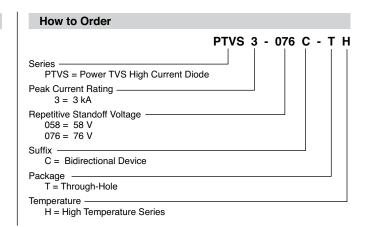
Epoxy encapsulation materials conform to UL 94V-0. Silver plated lead finish conforms to the solderability requirements of JESD22-B102, Pb free solder. Package dimensions are shown below:



Dim.	PTVS3-058C-TH	PTVS3-076C-TH			
Α	24.15 ± 0.72				
A	$\overline{(0.951 \pm 0.028)}$				
В	2.40 ± 0.50				
	(0.094 ± 0.020)				
С	1.75 ±	± 1.25_			
	(0.069 ± 0.049)				
D	10.80	Max.			
	(0.425) Wax.				
Е	1.25 ± 0.05				
	(0.049 ± 0.002)				
F 9.30 Max.					
ı	(0.366) IVIAX.				
G	$\frac{5.00}{(2.100)}$ Max.	$\frac{6.00}{(0.000)}$ Max.			
	(0.197) Wax.	(0.236) Wax.			
Н	6.00 ± 1.00				
	(0.236 ± 0.039)				

Typical Part Marking

PTVS3-058C-TH	3058
PTVS3-076C-TH	3076



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PTVS3-076C-TH PTVS3-058C-TH